

Fig. 1

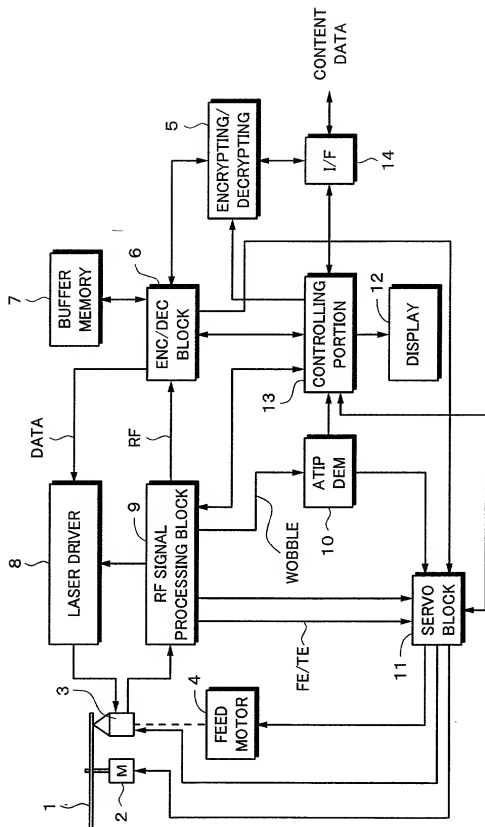


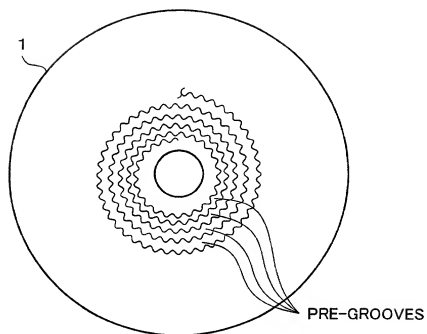
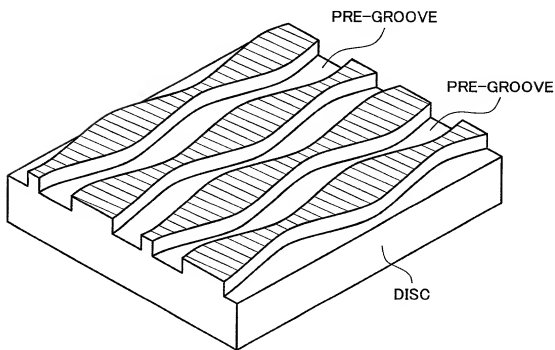
Fig. 2**Fig. 3**

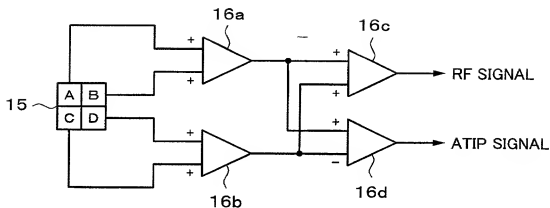
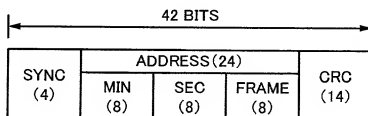
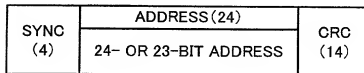
Fig. 4**Fig. 5A****Fig. 5B**

Fig. 6

M1	S1	F1	CONTENTS OF INFORMATION	
			ADDRESS OF PROGRAM AREA AND LEAD-OUT AREA	
0	0	0	ADDRESS OF LEAD-IN AREA	
1	0	0	SPECIAL INFORMATION 1 : WRITE POWER AT REFERENCE SPEED	
1	0	1	SPECIAL INFORMATION 2 : START ADDRESS OF LEAD-IN AREA	
1	1	0	SPECIAL INFORMATION 3 : LAST START ADDRESS OF LEAD-OUT AREA	
1	1	1	ADDITIONAL INFORMATION 1 : SPEED RANGE, OPC PARAMETER, ERASE POWER	
0	0	1	ADDITIONAL INFORMATION 2 : NOT USED (RESERVED)	
0	1	0	ADDITIONAL INFORMATION 3 : NOT USED (RESERVED)	
0	1	1	ADDITIONAL INFORMATION 3 : NOT USED (RESERVED)	

Fig. 7

FRAME NUMBER	CONTENTS OF FRAME
N	SPECIAL INFORMATION 1
N+1 : N+9	REGULAR ADDRESS
N+10	SPECIAL INFORMATION 2
N+11 : N+19	REGULAR ADDRESS
N+20	SPECIAL INFORMATION 3
N+21 : N+29	REGULAR ADDRESS
N+30	ADDITIONAL INFORMATION 1
N+31 : N+39	REGULAR ADDRESS
N+40	SPECIAL INFORMATION 1
N+41 : N+49	REGULAR ADDRESS
N+50	SPECIAL INFORMATION 2
N+51 :	REGULAR ADDRESS

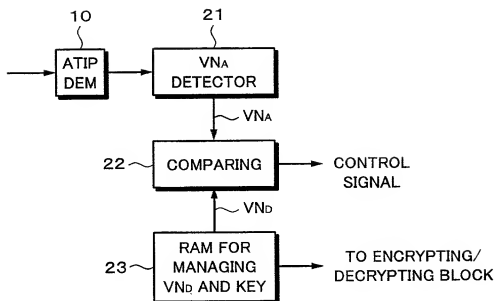
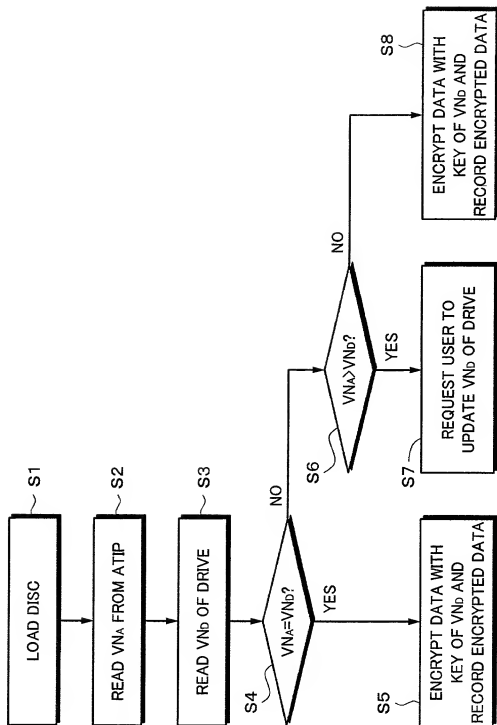
Fig. 8

Fig. 9



DESCRIPTION OF REFERENCE NUMERALS

- 1 OPTICAL DISC
- 3 - OPTICAL PICKUP
- 8 LASER DRIVER
- 9 RF SIGNAL PROCESSING BLOCK
- 10 ATIP DEMODULATOR
- 13 CONTROLLING PORTION